DCI/ICS 86-4013 22 January 1986

	MEMORANDUM FOR:	Members, COMPL	JSEC Threat Senio	r Executive Par	nel	
25 X 1	FROM:		DCI's COMPUSEC	Project Directo	or	
	SUBJECT:	COMPUSEC Threat Assigned Areas of Responsibility				
25X1	1. Attached are two items: the list of assigned areas of responsibility, with proposed briefing dates to the Panel, and a copy of the Table of Contents, as agreed upon at our last meeting. The next two meetings of the Panel are 18 February and 26 March, 1986, and both are from 0900-1630,					
25 X 1	2. Please	review the prop	osed schedule and	let	know if you	
cannot be available to brief at the times for which you are scheduled. 25X1 can be reached at 25X1						
	Attachments:		-			
	As stated					
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COMPUSEC THREAT II SENIOR EXECUTIVE PANEL Areas of Responsibility and Briefing Schedule

	Ι.	Findings and Recommendations	On hold	
	II.	The Issue 1985-1986 Focus	On hold	
	III.	Describing the Threat	Bush/PRC	March 26
	IV.	The Target Population	IC Staff/IHC	March 26
√	٧.	1985-1986 Critical Threat Target Areas Characteristics and Assessments	Bush/PRC	Feb. 18
	VI.	Critical SystemsCurrent Status	On hold	
	VII.	Security Shortfall Correlation for Critical Systems	On hold	
,	√vIII.	Threats to Processors	Adams/LANL	Feb. 18
	IX.	Threats to Networks	Lockwood/BETAC	March 26
	Χ.	Supercomputers and Parallel Processors	On hold	
	XI.	Threats to "Advanced Concept" Systems	On hold	
	XII.	Implantation of Helping Mechanisms	CIA/NSA/LANL	March 26
	✓XIII.	Terrorism and Hacking Threat	Tate/RTA	Feb. 18
	√XIV.	Error and Malfunction	Donnelly /CIA	Feb. 18
✓ XV.		Countermeasures (SAFEGUARDS)	DIA	Feb. 18
	XVI.	Generic Problem Areas	Donnelly CATA	March 26
	XVII.	Needs for Proofs of Security	On hold	
	XVIII.	Future Trends	On hold	
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